

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,071,551 B1
APPLICATION NO. : 10/030123
DATED : July 4, 2006
INVENTOR(S) : Yasuji Hiramatsu et al.

Page 1 of 1

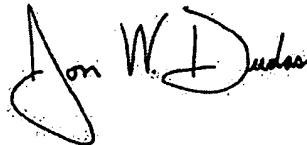
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 22, line 52, change "comprises:" to --comprising:--.

Column 23, Line 7, change "The device used to produce or examine according to" to
-- The device used to produce or examine semiconductors according to--.

Signed and Sealed this

Ninth Day of January, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office